

FIG. 1

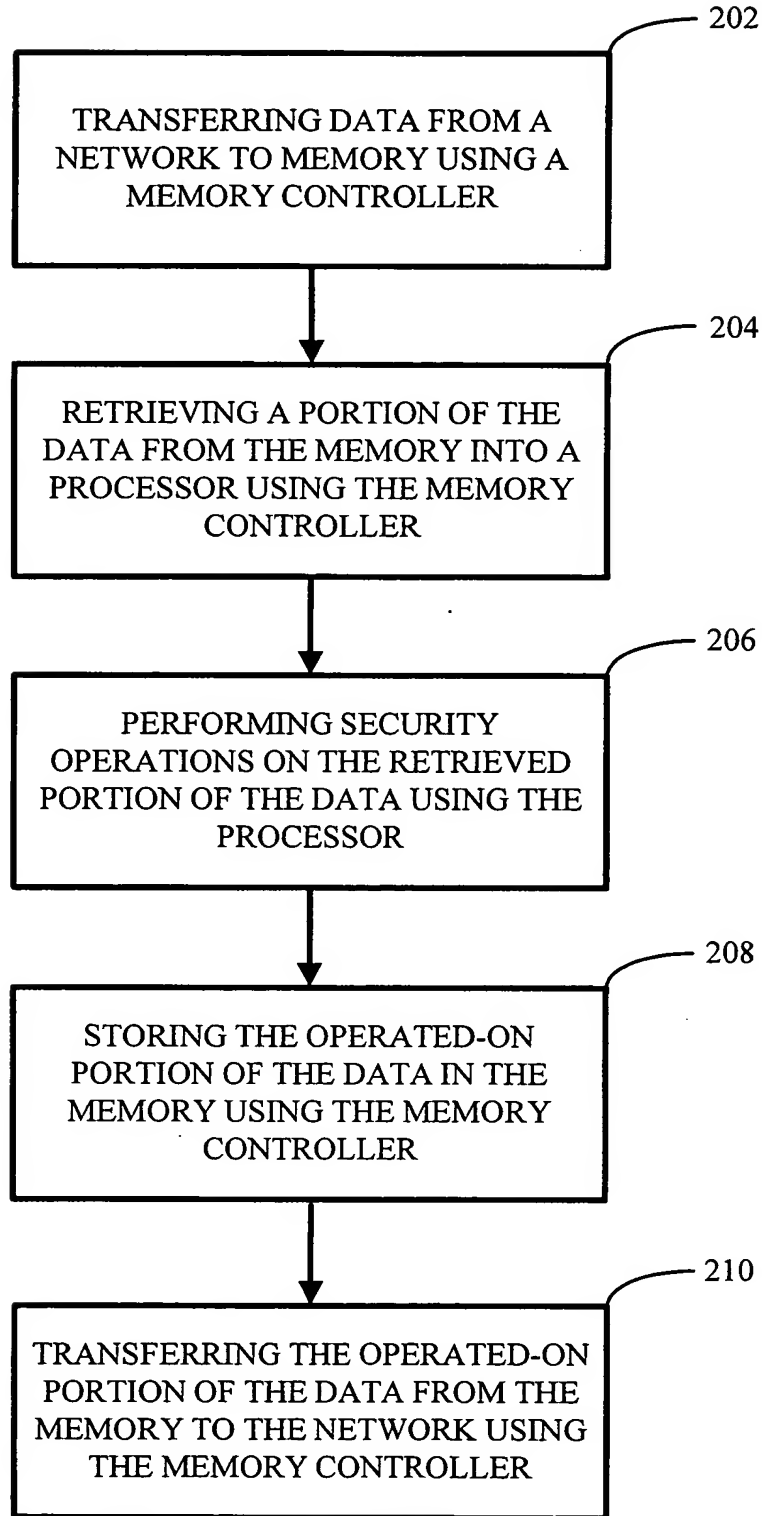


FIG. 2

EMBEDDED PROCESSOR COMPLEX

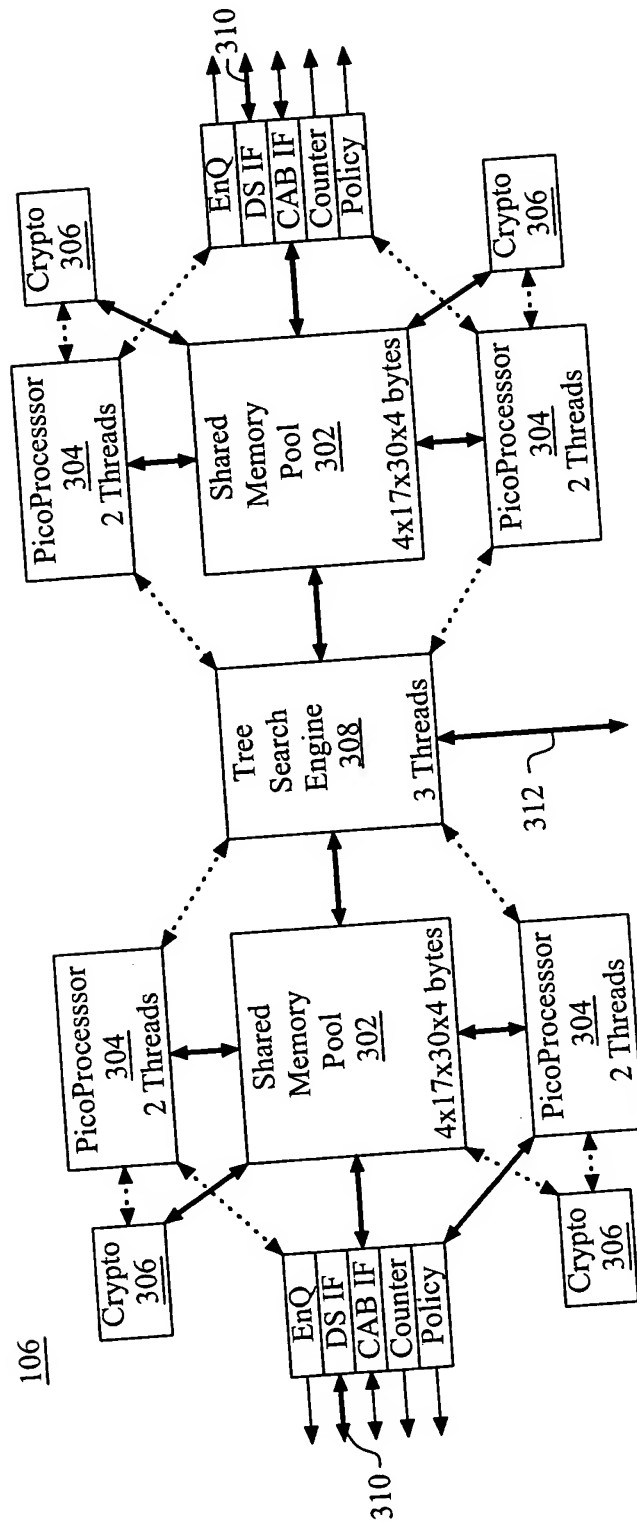
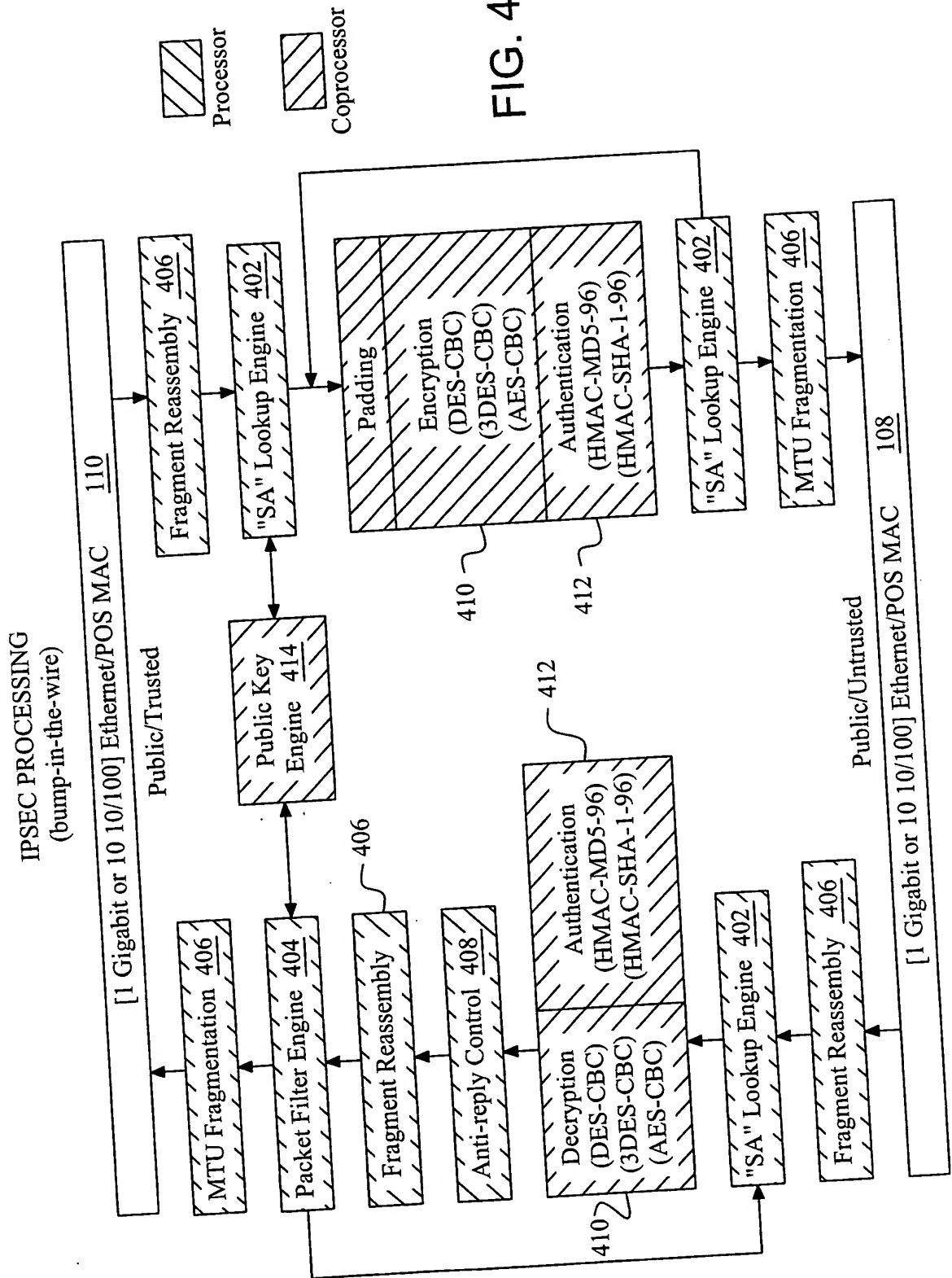


FIG. 3



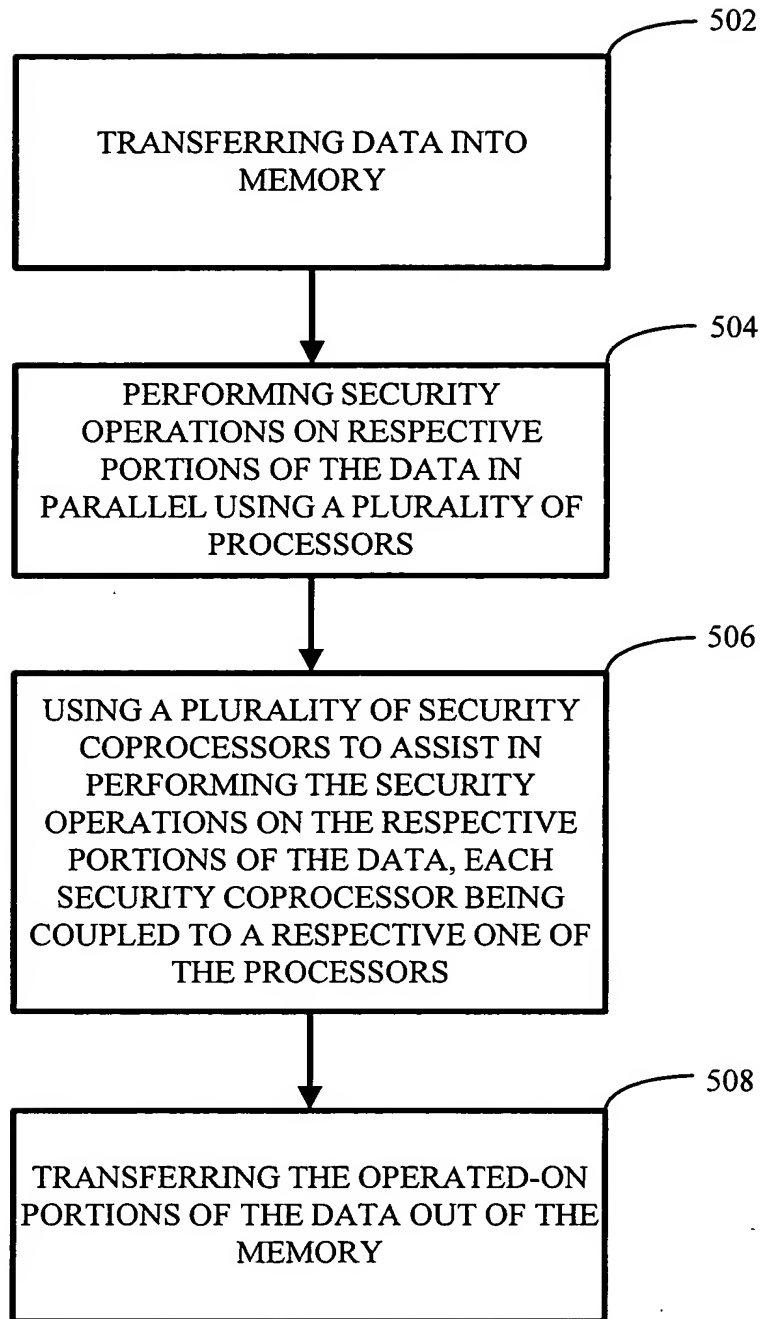


FIG. 5

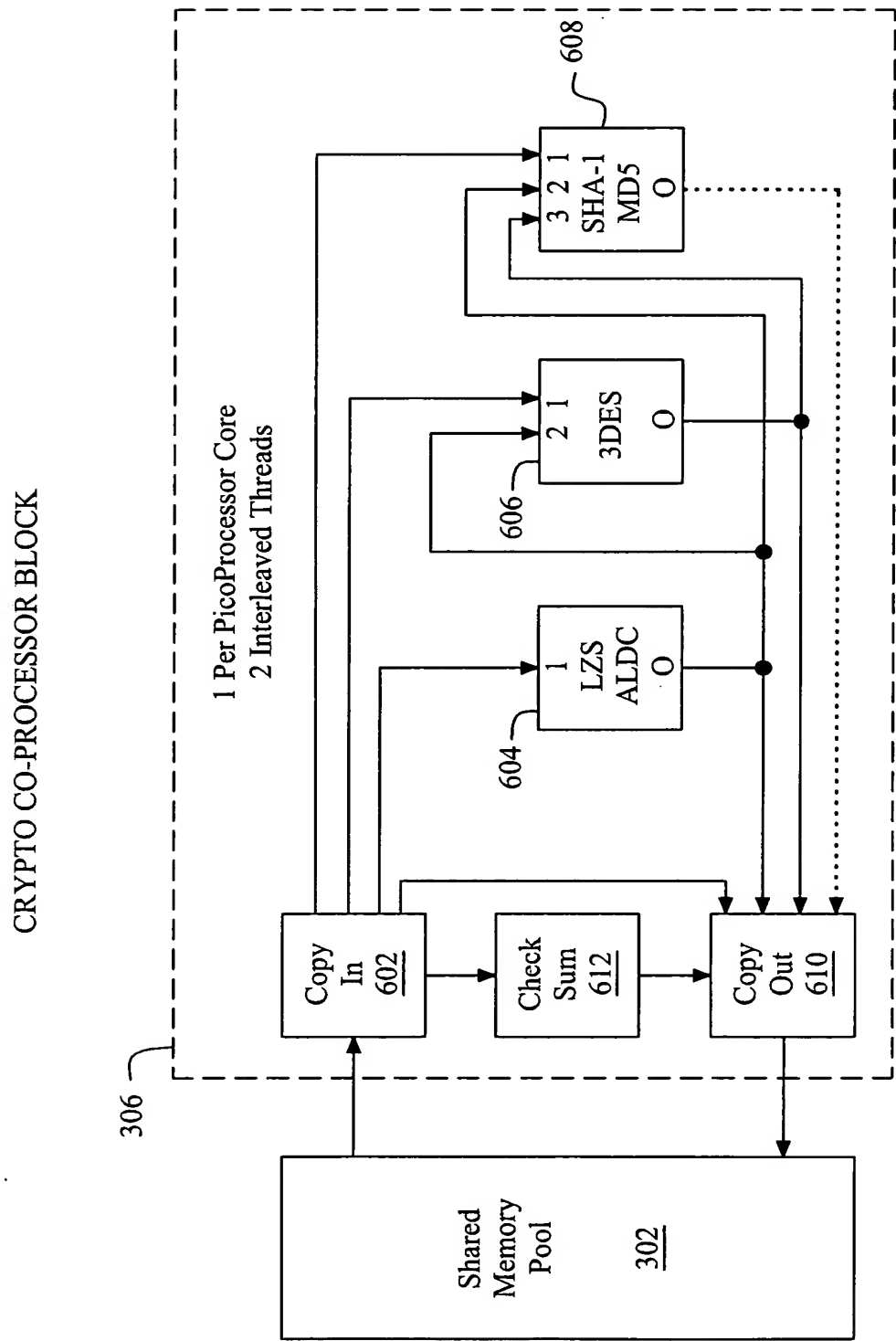


FIG. 6

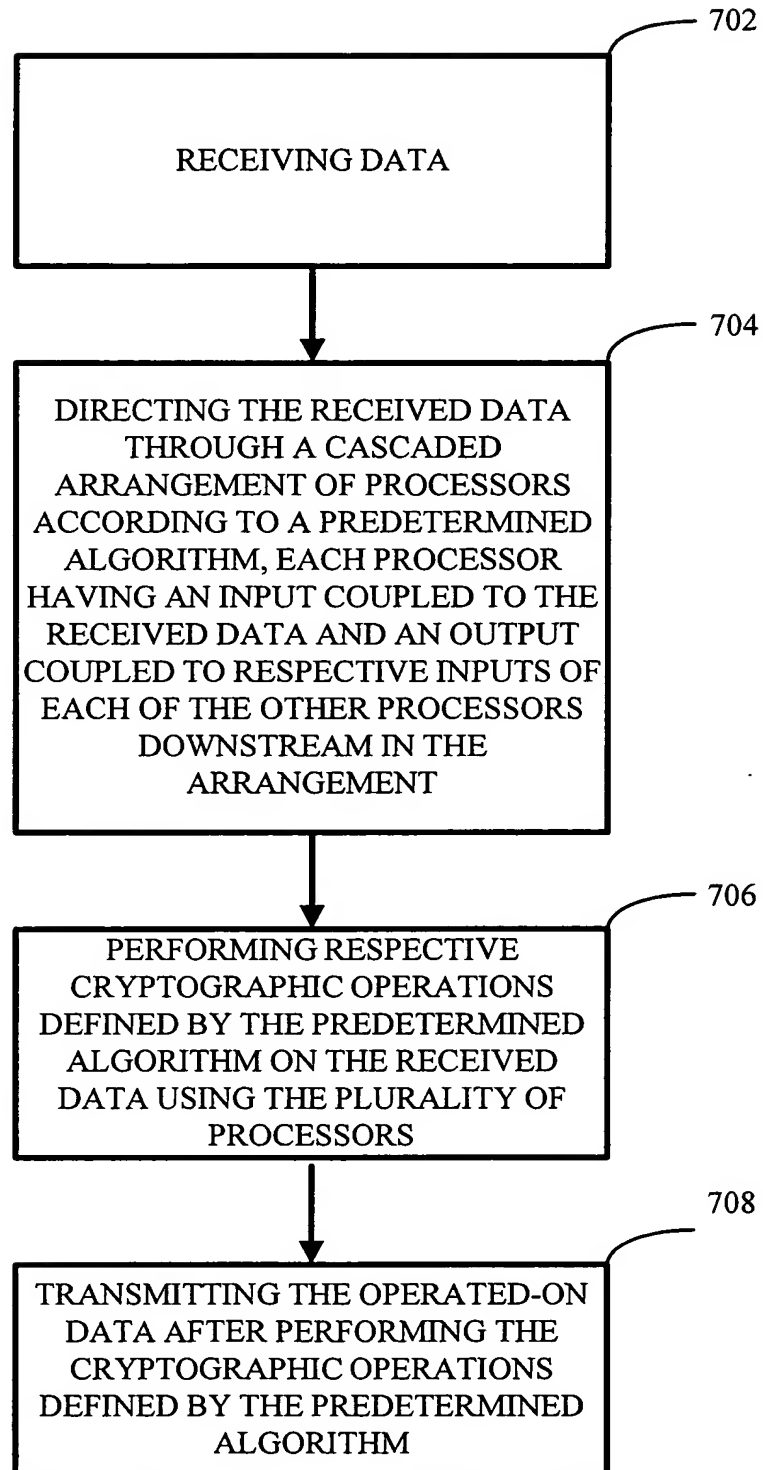


FIG. 7